



iPower 90 500W



ErP Lot 6 2013

MODEL SELECTION

Part Number	PFC	Input Voltage
PCB008	Active PFC	100-240Vac

FEATURES

- Built with SRC architecture
- 12V to 5V/3.3V SR D-D VRM layout
- Support Intel Core i5 & i7 CPUs
- Compliance with ErP Lot 6 2013: 5Vsb AC input < 0.5W at standby & off mode
- Solid state capacitor for DC output
- Support 5KM⁺ area usage of China CCC regulation
- Full range input with active PFC
- 90% high efficiency
- Support AMD Phenom & Athlon CPUs

GENERAL SPEC.

Hold-up Time	16ms@80% load
Dimensions	150 x 150 x 86 (mm)
MTBF	100KHrs
Output Rise Time	0.2~20ms
PG Signal Rise Time	<1ms

ENVIRONMENTAL SPEC.

Operating Temperature	0 ~ 40°C
Storage Temperature	-40 ~ 70°C

OUTPUT CURRENT CAPACITY

Voltage	Regulation	Min. Load	Max. Load	Combined
+5V	4.75~5.25	0A	20A	120W
+3.3V	3.13~3.46	0A	20A	
+12V1	11.4~12.6	0.1A	18A	480W
+12V2	11.4~12.6	0.05A	18A	
+12V3	11.4~12.6	0.0A	18A	
-12V	-10.8~-13.2	0A	0.3A	3.6W
+5Vsb	4.75~5.25	0.05A	2.5A	12.5W

INPUT SPEC.

Input Voltage Range	100-240Vac
Input Frequency Range	50-60Hz
AC Input Current	7A@115V; 3.5A@230V
PFC (Harmonic)	Meet EN61000-3-2 Class D
EMI/RFI	CISPR 22 Class B

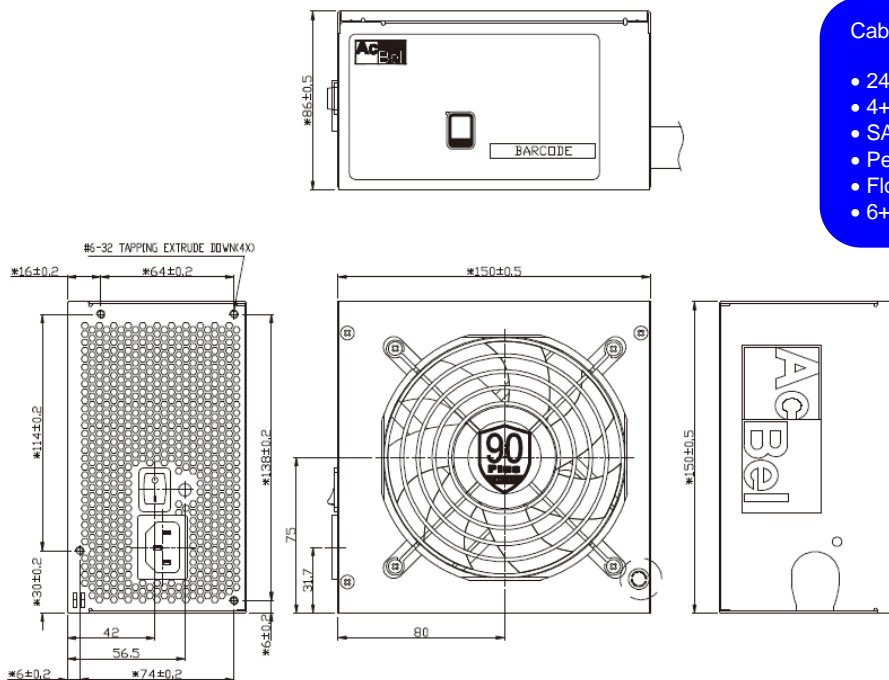
OUTPUT SPEC.

Output Power	500W
Efficiency	87%@20% load; 90%@50% load; 87%@100% load
OVP	+3.3V:4.5V, +5V:7V, +12V:16V
OCP	+3.3V, +5V, +12V shut down at each output
OTP	Latch off all main outputs if over temperature
SCP	Latch off all main outputs reset by cycling on/off control or AC power

SAFETY

TUV, CE, CCC, CB, UL, FCC, BSMI, CTick

OUTLINE DRAWING



Cabling:

- 24pin for M/B x 1
- 4+4pin for CPU x 1
- SATA x 7
- Peripheral 4pin x 4
- Floppy 4pin x 1
- 6+2 to 6+2pin PCI-E x 2

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i. Specifications are subject to change without prior notice
 ii. All illustrations are for reference only.

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